

### 40G QSFP+ SR4 Transceiver

### 1. <u>SCOPE</u>

This product is a parallel 40Gb/s Quad Small Form-factor Pluggable (QSFP+) optical module. It provides increased port density and total system cost savings. The QSFP+ full-duplex optical module offers 4 independent transmit and receive channels, each capable of 10Gb/s operation for an aggregate data rate of 40Gb/s on 100 meters of OM3 multi-mode fiber. An optical fiber ribbon cable with an MTP/MPO connector can be plugged into the QSFP+ module receptacle.

### 2. PRODUCT FEATURES

- Four-channel full-duplex transceiver module
- Hot Pluggable QSFP+ form factor
- Maximum link length of 100m on OM3 Multimode Fiber(MMF) and 150m on OM4 MMF
- Multirate capability: 1 Gb/s to 10.5 Gb/s per channel
- Unretimed XLPPI electrical interface
- Maximum power dissipation <1.5W
- Reliable VCSEL array technology
- Commercial operating case temperature range: : 0°C to 70°C
- RoHS-6 Compliant

### 3. PRODUCT DESCRIPTION

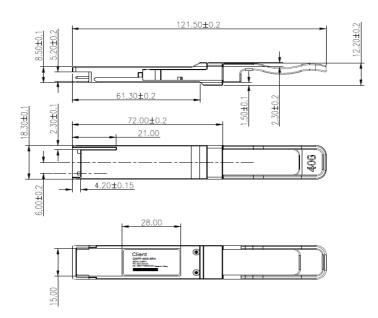
#### 3.1 PRODUCT NAME AND SERIES NUMBER(S)

40G QSFP+ SR4 Transceiver										
Part Number	Data Rate	Wavelength (nm)	Distance	Media	Power (dBm)	Sen. (dBm)	Connector	Tem.		
ZFTES4E0850A5ST	40G	850	100m	MMF	-7.6 ~ 2.4	-9.5	MPO	С		



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#### 3.2 DIMENSIONS, MATERIALS, PLATINGS AND MARKING



Unit is millimeter. All dimensions are ±0.1mm unless otherwise specified.

### 4. APPLICABLE DOCUMENTS AND SPECIFICATIONS

• QSFP+ MSA compliant

### 5. QUALIFICATION

- Electrostatic Discharge (ESD) to the Electrical Pins
- Electrostatic Discharge (ESD) to the MPO Connector
- RoHS compliance

### 6. Absolute Maximum Ratings & Recommended Operating Conditions

Absolute Maximum Ratings							
Parameter	Symbol	Min.	Max.	Unit	Notes		
Storage Temperature	Ts	-40	85	degC			
Operating Case Temperature	TOP	0	70	degC			
Power Supply Voltage	Vcc	-0.5	3.6	V			



# 40G QSFP+ SR4 Transceiver

Relative Humidity (non-condensation)	RH	0	85	%	1
Receiver Damage Threshold per Lane	PIND	3.4		dBm	

Recommended Operating Conditions							
Parameter	Symbol	Min.	Typical	Max.	Unit	Notes	
Operating Case Temperature	TOP	0		70	degC		
Power Supply Voltage	Vcc	3.135	3.3	3.465	V		
Data Rate, each Lane			10.3125	11.2	Gb/s		
Link Distance (OM3)	D			100	m		
Power Consumption				1.5	W		
Supply Current	lcc			450	mA		
Transceiver Power-on Initialization Time				2000	ms	2	

#### Notes:

Non-condensing.
 From power-on and end of any fault conditions.

Transmitter Operating Characteristic-Optical, Electrical						
Parameter	Symbol	Min.	Typical	Max.	Unit	Note
	Optical	Characte	istics			
Signaling Speed per Lane			10.5		GBd	1
Center Wavelength	λC	840	850	860	nm	
RMS Spectral Width	Δλrms			0.65	nm	
Average Launch Power, each Lane	PAVG	-7.6		1.0	dBm	
Optical Modulation Amplitude (OMA), each Lane	РОМА	-5.6		3.0	dBm	2
Difference in Launch Power between any Two Lanes (OMA)	Ptx,diff			4.0	dB	



# 40G QSFP+ SR4 Transceiver

Peak Power, each Lane	PPT			4.0	dBm		
Extinction Ratio	ER	3.0			dB		
Average launch power of OFF transmitter, per lane				-30	dBm		
Transmitter eye mask definition {X1, X2, X3, Y1, Y2, Y3}		0.23, 0.3	4, 0.43, 0.27,	0.35, 0.4			
	Electrica	I Characte	ristics		· · ·		
Single ended input voltage tolerance	VinT	-0.3		4.0	V		
Differential data input swing	Vin,pp	180		1200	mVpp	3	
Eye mask colordinates {X1, X2		0.11, 0.31			UI		
Y1, Y2}			95, 350		mV	4	

Notes:

1. Transmitter consists of 4 lasers operating at a maximum rate of 10.5Gb/s each.

2. Even if TDP is <0.9dB, the OMA min must exceed this value

3. After internal AC coupling. Self-biasing 100 differential input.

4. Hit ratio = 5 x 10E-5

Receiver Operating Characteristic-Optical, Electrical									
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note			
	Optical Characteristics								
Signaling Speed per Lane			10.5		GBd	1			
Center wavelength		840	850	860	nm				
Damage Threshold	DT	3.4			dBm				
Average Receive Power per Lane	RXP <sub>x</sub>	-9.9		2.4	dBm				
Receive Power (OMA) per Lane	RxOMA			3.0	dBm				
Peak Power, per lane	PPx			4	dBm				
LOS De-Assert	LOSD			-12	dBm				



# 40G QSFP+ SR4 Transceiver

LOS Assert	LOSA	-30			dBm		
LOS Hysteresis	LOSH	0.5			dBm		
	Electrical Characteristics						
Single-ended output voltage		-0.3		4.0	V		
Differential data output swing	Vout,pp	0		800	mVpp	4,5	
Output transition time, 20% to 80%		28			ps		
Eye mask coordinates #1 {X1, X2 Y1, Y2}			0.29, 0.5 150, 425	I	UI mV	3	

Notes:

- 1. Receiver consists of 4 photodetectors operating at a maximum rate of 10.5Gb/s each.
- 2. 10 MHz to 11.1 GHz range
- 3. *Hit ratio* = 5 x 10E-5.
- 4. AC coupled with 100 differential output impedance.
- 5. Settable in 4 discrete steps via the I2C interface.

### **<u>6. Digital Diagnostic Functions</u>**

The following digital diagnostic characteristics are defined over the Recommended Operating Environment unless otherwise specified. It is compliant to SFF-8436.

Parameter	Symbol	Min	Мах	Units	Notes
Temperature monitor absolute error	DMI_Temp	-3	3	degC	Over operating temperature range
Supply voltage monitor absolute error	DMI_VCC	-0.1	0.1	V	Over full operating range
Channel RX power monitor absolute error	DMI_RX_Ch	-3	3	dB	1
Channel Bias current monitor	DMI_Ibias_Ch	-10%	10%	mA	Ch1~Ch4
Channel TX power monitor absolute error	DMI_TX_Ch	-3	3	dB	1

Notes:

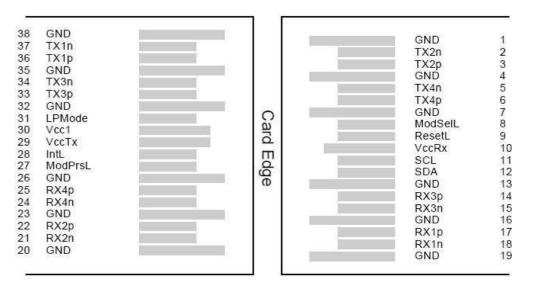
Due to measurement accuracy of different single mode fibers, there could be an additional +/-1 dB fluctuation, or a +/- 3 dB total accuracy.



### **PAGE 6/9**

# TITLE

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Top Side Viewed from Top Bottom Side Viewed from Bottom

#### **Pin Definitions**

### 7. Applications Note :

#### **Pin Assignment**

PIN	Logic	Symbol	Name/Description	Notes
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-Inverted Data output	
7		GND	Ground	1
8	LVTLL-I	ModSelL	Module Select	
9	LVTLL-I	ResetL	Module Reset	
10		VccRx	+3.3V Power Supply Receiver	2
11	LVCMOS-I/O	SCL	2-Wire Serial Interface Clock	
12	LVCMOS-I/O	SDA	2-Wire Serial Interface Data	
13		GND	Ground	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	



# 40G QSFP+ SR4 Transceiver

15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	1
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		VccTx	+3.3 V Power Supply transmitter	2
30		Vcc1	+3.3 V Power Supply	2
31	LVTTL-I	LPMode	Low Power Mode	
32		GND	Ground	1
33	CML-I	ТхЗр	Transmitter Non-Inverted Data Input	
34	CML-I	Tx3n	Transmitter Inverted Data Output	
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Output	
38		GND	Ground	1



### 40G QSFP+ SR4 Transceiver

#### Notes:

- GND is the symbol for signal and supply (power) common for QSFP+ modules. All are common within the QSFP+ module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.
- VccRx, Vcc1 and VccTx are the receiver and transmitter power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown in Figure 4 below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP+ transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.

#### **Optical Interface Lanes and Assignment**

Figure 3 shows the orientation of the multi-mode fiber facets of the optical connector. Table 1 provides the lane assignment.

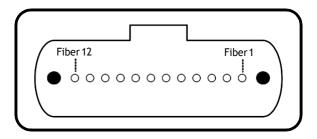


Figure 3. Outside View of the QSFP+ Module MPO



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#### Table 1: Lane Assignment

Fiber #	Lane Assignment
1	RX0
2	RX1
3	RX2
4	RX3
5,6,7,8	Not used
9	ТХЗ
10	TX2
11	TX1
12	TX0

#### **Recommended Interface Circuit**

